# Three-Phase Full-Wave Motor Drivers for Cylinder Motors

### 3-phase motor driver for VCR cylinder motors

### BA6464FP-Y

The BA6464FP-Y is a 3-phase motor driver designed for VCR cylinder motors. It contains a power supply with a constant voltage of 5V, a short brake circuit, and a start/stop pin to open the output pins.

### Applications

VCR cylinder motors

### Features

- 1) 3-phase, full-wave, pseudo-linear drive system.
- 2) Internal constant voltage power supply. (5V)
- 3) Short brake circuit.
- 4) Internal thermal shutdown circuit.

### ●Absolute maximum ratings (Ta=25°C)

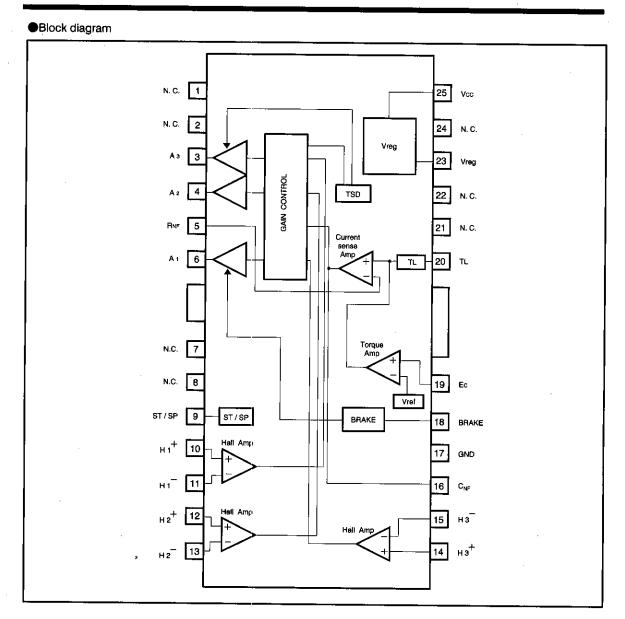
Parameter	Symbol	Limits	Unit V	
Applied voltage	Vcc	24		
Power dissipation	Pd	1450*1	mW	
Operating temperature	Topr	-20~75	င	
Storage temperature	Tstg	_55 <b>~</b> 150	ర	
Output current	lout	1000*2	mA	

<sup>\*1</sup> Mounted on a glass epoxy PCB (90 X 50 X 1.6 mm). Reduce power by 11.6 mW for each degree above 25  $^{\circ}\mathrm{C}$  .

### ●Recommended operating conditions (Ta=25℃)

Parameter	Symbol	Limits	Unit
Operating power supply voltage	Vcc	7.5~20.0	V

<sup>\*2</sup> Should not exceed Pd- or ASO-value.



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### ●Pin descriptions

Pin No.	Pin name	Function
1	N.C	Not used
2	N.C	Not used
3	Аз	Output pin
4	A <sub>2</sub>	Output pin
5	R <sub>NF</sub>	Output current sensing pin; normally a resistor (0.5 $\Omega$ is connected between this pin and the ground)
6	A <sub>1</sub>	Output pin
7	N.C	Not used
8	N.C	Not used
9	ST / SP	Start/stop switching pin; start mode when HIGH
10	H <sub>1</sub> +	Hall signal input pin
11	H1-	Hall signal input pin
12	H <sub>2</sub> +	Hall signal input pin
13	H <sub>2</sub> -	Hall signal input pin
14	H <sub>3</sub> +	Hall signal input pin
15	H <sub>3</sub> <sup>-</sup>	Hall signal input pin
16	C <sub>NF</sub>	Capacitor connection pin for phase compensation; set the capacitor so that the output does not oscillate
17	GND	GND
18	BRAKE	BRAKE pin; connected to the € pin
19	Ec	Output current control pin; controls the motor current
20	TL	Torque limit pin; controls the motor current
21	N.C	Not used
22	N.C	Not used
23	V <sub>reg</sub>	Internal constant voltage (5 V) output pin
24	N.C	Not used
25	Vcc	Power supply pin
FIN	FIN	Be sure to connect this fin to the ground

### Input/output circuits

Pin no.	Pin name	Pin description	I/O equivalent circuit
9	ST/SP	Start/stop switching pin	□ 26kΩ ₩ 10kΩ ≶
20	TL	Torque limit pin	1kΩ W
19	Ec	Output current control pin	Vcc ⊕ ₩ ↓ ↓ ↓ ↓ ↓
3, 4, 6	A3, A2, A1	Output pin	Voc
10, 11, 12, 13, 14, 15	H <sub>1</sub> +, H <sub>1</sub> -, H <sub>2</sub> + H <sub>2</sub> -, H <sub>3</sub> +, H <sub>3</sub> -	Hall signal input pin	1k0 W
18	BRAKE	Brake pin	1kg

### ●Electrical characteristics (Unless otherwise noted, Ta=25°C, Vcc=12V)

Parameter	Symbol	Mln.	Тур.	Max.	Unit	Conditions
Circuit current	lcc	_	6.0	11.0	mA	
Constant output voltage	V <sub>reg</sub>	4.5	5.0	5.5	٧	
~MDA~						
Hall input bias current	Інв	_	0.25	5.0	μΑ	H <sup>+</sup> =Hi, H <sup>-</sup> =Low or H <sup>+</sup> =Low, H <sup>-</sup> =Hi
Hall input common-mode voltage	Vнв	1.5	_	V <sub>reg</sub> 1.0	٧	
Hall device minimum input level	V <sub>INH</sub>	60	_	-	mV <sub>P-P</sub>	
Torque control input voltage	Ec	0	_	V <sub>reg</sub>	٧	
Torque control voltage offset	Ecofs	-150	0	+150	mV	For Ec = Vreg X 0.46
Output idle voltage	Ecidie	_	0	10	mV	Ec=2V, R <sub>NF</sub> =0.5Ω
I/O gain	GEC	0.5	0.6	0.7	A/V	Measured at Ec = 2.8 V, 3.3 V $R_{NF}$ =0.5 $\Omega$
Start/stop pin threshold voltage	V <sub>s/s</sub> th	1.2	2.35	3.5	<b>V</b> .	
HIGH level output saturation voltage	V <sub>он</sub>		1.05	1.75	٧	lo=600mA
LOW level output saturation voltage	Vol	_	0.45	1.05	٧	Io=600mA
Output drive current capacity	lo (Max.)	800			mA	R <sub>NF</sub> =0.5Ω Tj=25°C *
Torque limit current	· I <sub>TL</sub>	520	650	780	mA	TL=0.4V, Rn=0.5Ω
~BRAKE~						
Brake pin threshold voltage	V <sub>BRK</sub> th	1.5	1.6	1.7	٧	When Vreg = 5V

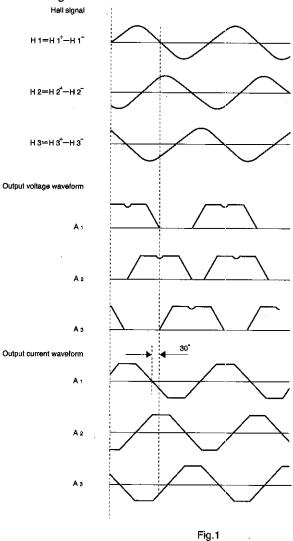
<sup>\*</sup> TJ is chip junction temperature.

O Not designed for radiation resistance.

### ●Circuit operation

### (1) Hall I/O

The 3-phase Hall signal is amplified in the hall amplifiers and sent to the matrix section, where the signal is further amplified and combined. After the signal is converted to a current in the amplitude control circuit, the current is supplied to the output driver, which then provides a motor drive current. The phases of the Hall input signal, output voltage, and output current are shown in Fig. 1.



### (2) Torque control pin (EC pin)

The output current can be controlled by adjusting the voltage applied to the torque control pin.

## Output current Gradient (servo input gain) 0.6 A / V Ec (V)

Fig.2

### (3) Start/stop pin

The motor is in the run mode when the pin input voltage is 3.5V or more and in the idle mode (all output transistors are OFF) when the voltage is 1.2V or less.

### (4) Power ground pin (RNF pin)

The  $R_{NF}$  pin is the output stage ground pin. Connect a resistor (0.5  $\Omega$  recommended) between this pin and the ground to monitor the output current.

### (5) Phase compensation pin (CNF pin)

Connect a capacitor between this pin and Vcc if the output tends to oscillate.

### (6) BRAKE pin (BRAKE)

Connect this pin to the torque control pin.

### Application example

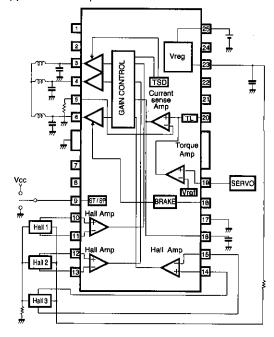
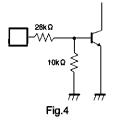


Fig.3

### Operation notes

### (1) Start/stop pin

The I/O equivalent circuit of the start/stop pin is shown in Fig. 4. The pin has a temperature dependence of -7mV/C, and the resistance can vary  $\pm$  30%. The voltage on this pin should be less than  $V_{\text{REG}}$ .



### (2) Hall input

The I/O equivalent circuit of the Hall input pins is shown in Fig. 5. The Hall devices can be connected in either series or parallel. The input Hall signal should be within the range of the Hall input common-mode voltage.

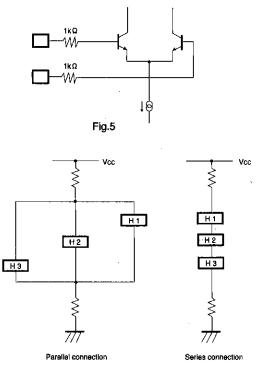
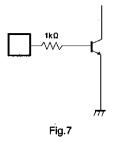


Fig.6

### (3) Torque limit pin

The output current can be limited by applying a voltage to the torque limit pin. Control is provided so that this pin will have the same potential as the power ground pin (RNF). Note that there is a voltage offset on this pin. The RNF-pin voltage is 0.325V when the TL-pin voltage is 0.4V (typical) and the RNF-pin resistance is  $0.5\,\Omega$ . Note that the voltage offset changes with the RNF-pin resistance. Connect the TL pin to VREG (pin 23) when the TL pin is not used.



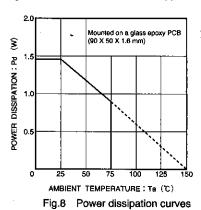
### (4) Brake pin

Connect this pin to the EC pin. Do not use the brake pin in the open state.

### (5) Thermal shutdown circuit

The circuit puts the driver outputs ( $A_1$ ,  $A_2$ , and  $A_3$ ) to the open state at the temperature of 175°C (typical). There is a temperature difference of about 20°C between the temperatures at which the circuit is activated and deactivated.

### Electrical characteristic curves



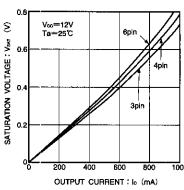


Fig.9 Low-side output saturation voltage vs.output current

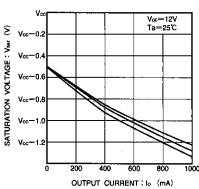
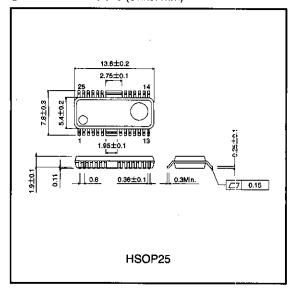


Fig.10 High-side output saturation voltaged vs. output current

### ●External dimensions (Units: mm)



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