

TENTATIVE TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8331AN

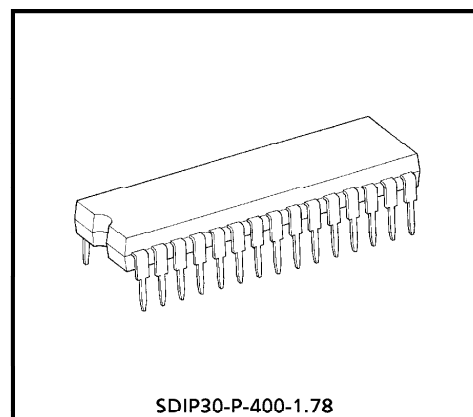
IH APPLIANCE CONTROLLER

TA8331AN is a control IC on a single chip dedicated to the control of the switching power supply, inverter, IGBT driver, and protector circuits in induction rice cookers and induction cookers.

The device is intended to reduce both the number of external components and the board size.

FEATURES

- Incorporates a switching power supply control circuit as a power supply for the IGBT driver block or DC fan motor or similar.
 - Incorporates an under-voltage lockout (UVLO) function.
(Startup voltage : 4.8V, shutdown voltage : 4.2V)
 - Incorporates an inverter control circuit for the IGBT driver, thus enabling the number of external components to be reduced.
 - Can directly drive the IGBT gate using a high current.
(Source current : -200mA (max), sink current 1A (max))
 - Incorporates a diode to protect the IGBT gate at power on.
 - Incorporates a comparator to detect low voltage, such as at an outage.
 - Incorporates various comparators for over-voltage detection.
 - Incorporates a function to detect over-voltages such as surges and to stop the IGBT drive signal.
- (Note) Since this product has a low electrostatic breakdown voltage, take great care when handling.

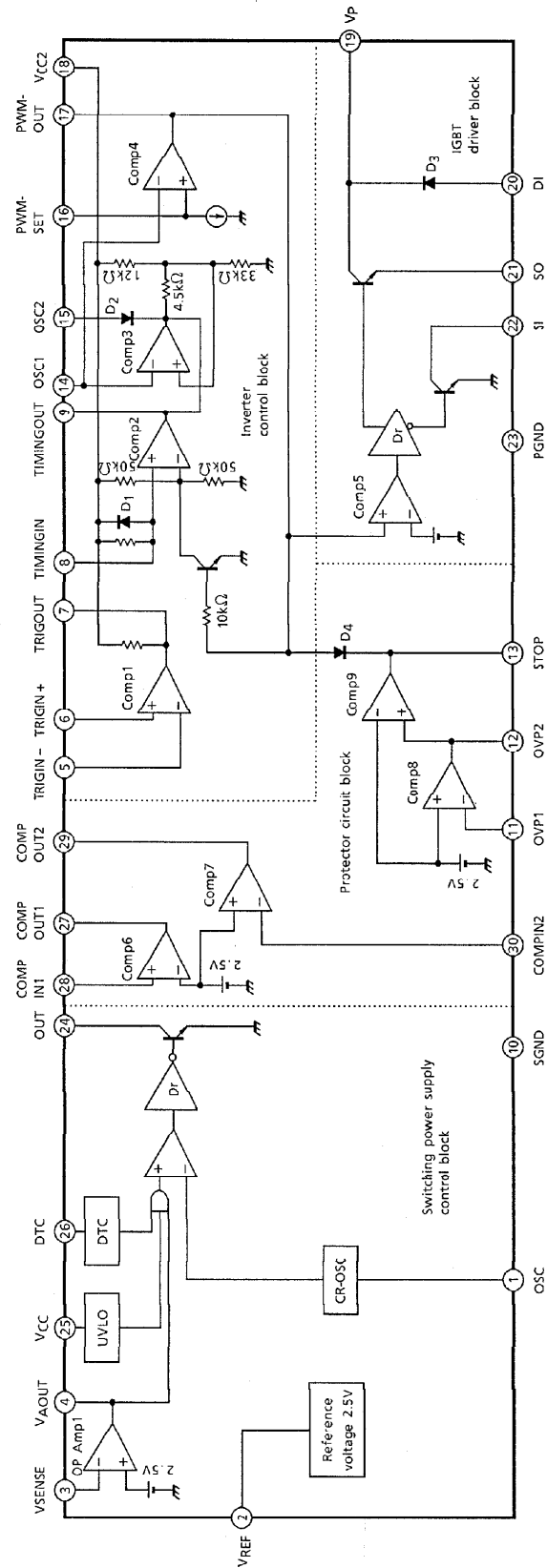


Weight : 1.99g (Typ.)

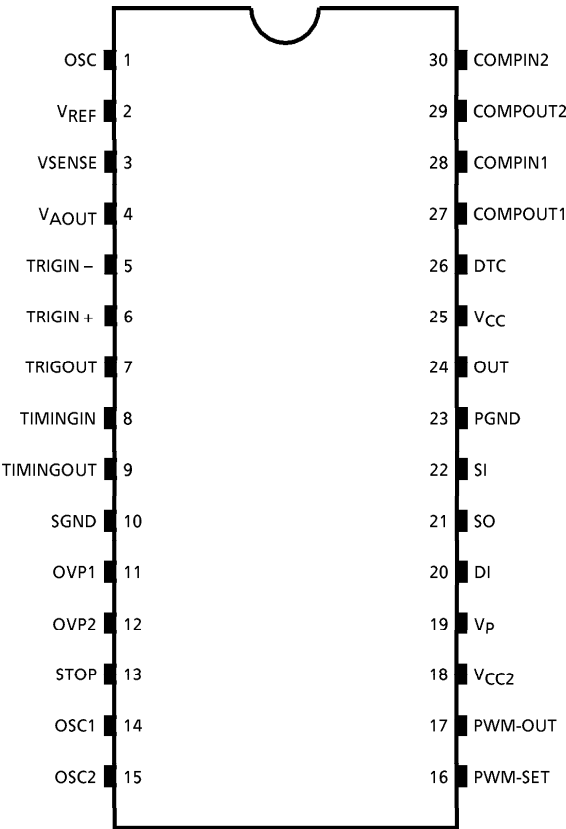
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BLOCK DIAGRAM



PIN CONNECTION



PIN FUNCTIONS

PIN No.	PIN NAME	FUNCTION
1	OSC	Oscillator connecting pin for switching power supply
2	VREF	Reference voltage output pin
3	VSENSE	Input pin for output voltage detection
4	VAOUT	Voltage error amp output pin
5	TRIGIN -	Trigger signal input pin 1 (- side)
6	TRIGIN +	Trigger signal input pin 2 (+ side)
7	TRIGOUT	Trigger signal output pin
8	TIMINGIN	Oscillation timing signal input pin
9	TIMINGOUT	Oscillation timing signal output pin
10	SGND	Signal GND
11	OVP1	Over-voltage detect input pin 1
12	OVP2	Over-voltage detect input pin 2
13	STOP	PWM stop signal output pin
14	OSC1	PWM oscillator connecting pin 1
15	OSC2	PWM oscillator connecting pin 2
16	PWM-SET	PWM output adjustment input pin
17	PWM-OUT	PWM signal output pin (open collector output)
18	VCC2	System power supply 2 (inverter control block/protector circuit block)
19	Vp	Power supply for power (IGBT driver block)
20	DI	IGBT gate protector diode pin
21	SO	IGBT gate drive pin 1 (source side)
22	SI	IGBT gate drive pin 2 (sink side)
23	PGND	Power GND
24	OUT	Switching power supply control output pin (open collector output)
25	VCC	System power supply (switching power supply block)
26	DTC	Dead time control pin
27	COMPOUT1	Comparator output pin 1 (for low voltage detection) (open collector output)
28	COMPIN1	Comparator input pin 1 (for low voltage detection)
29	COMPOUT2	Comparator output pin 2 (for over-voltage detection) (open collector output)
30	COMPIN2	Comparator input pin 2 (for over-voltage detection)

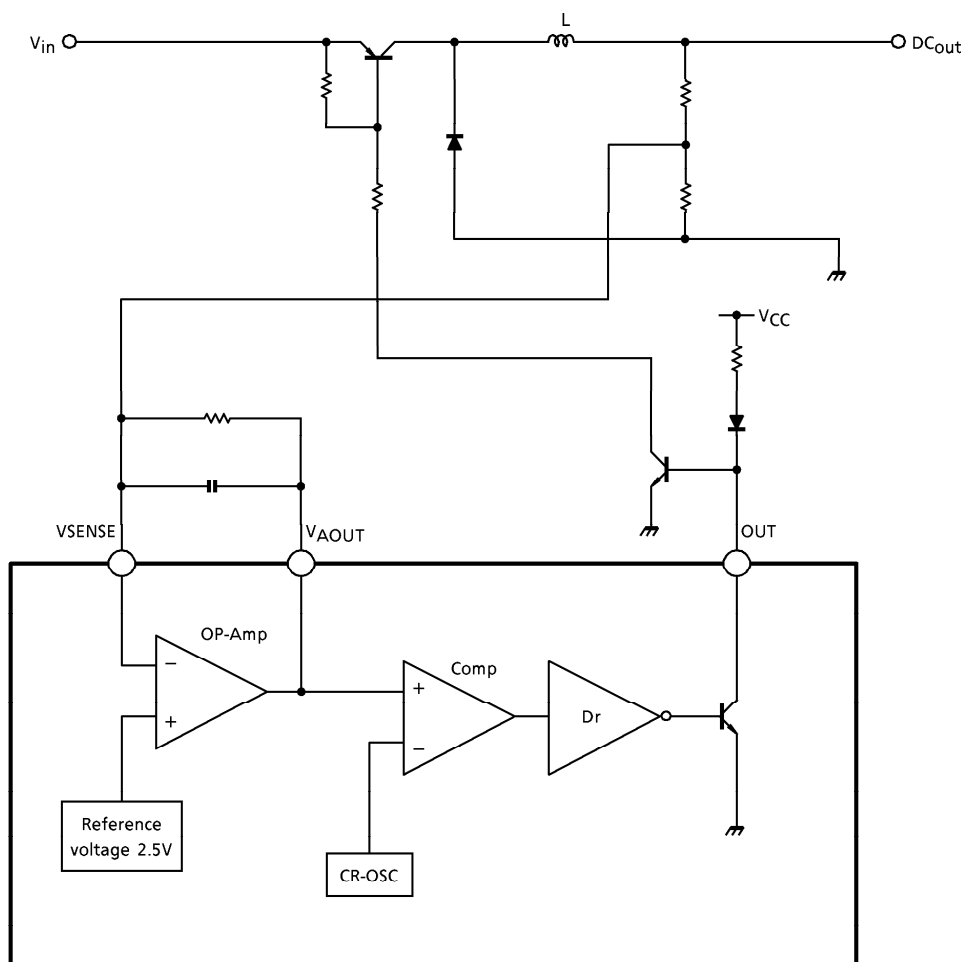
FUNCTIONS

1. Switching power supply control circuit

Incorporates a step-down switching power supply control function for the IGBT driver block power supply (V_p) or DC fan motor power supply.

When $DC_{out} = H$ ($V_{SENSE} = H$) : Output driver = ON (switching power supply = OFF)

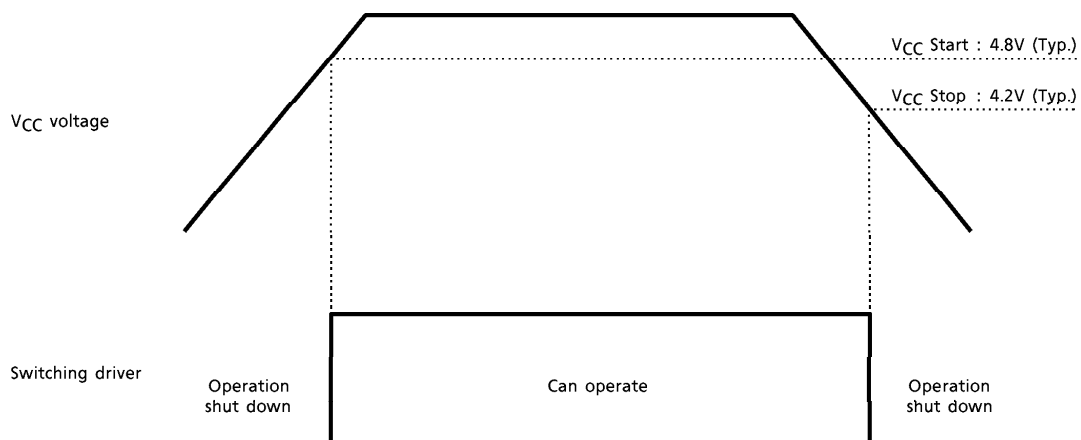
When $DC_{out} = L$ ($V_{SENSE} = L$) : Output driver = OFF (switching power supply = ON)



2. UVLO (Under-Voltage Lock Out) function

To prevent malfunction, a function is incorporated to shut down the switching power supply control block when the power supply voltage (V_{CC}) drops.

This function detects power supply voltage V_{CC} in the IC. If voltage is 4.8V or higher, the switching driver can operate. If voltage is 4.2V or lower, the switching driver is shut down.



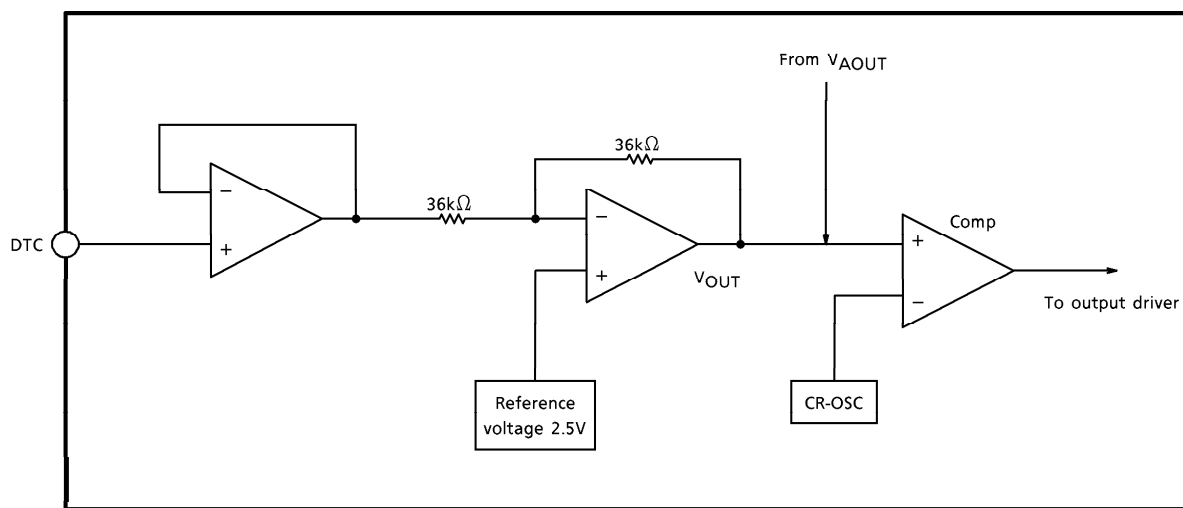
3. Dead time control function

Dead time control (DTC) is used to set the output pulse of the switching power supply control block to maximum duty.

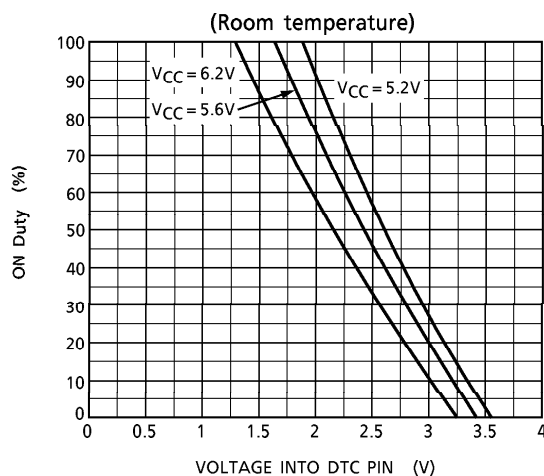
Depending on the DTC pin voltage, a maximum duty of up to 100% can be set.

DTC pin voltage low $\rightarrow V_{OUT}$ high \rightarrow output driver duty large

DTC pin voltage high $\rightarrow V_{OUT}$ low \rightarrow output driver duty small



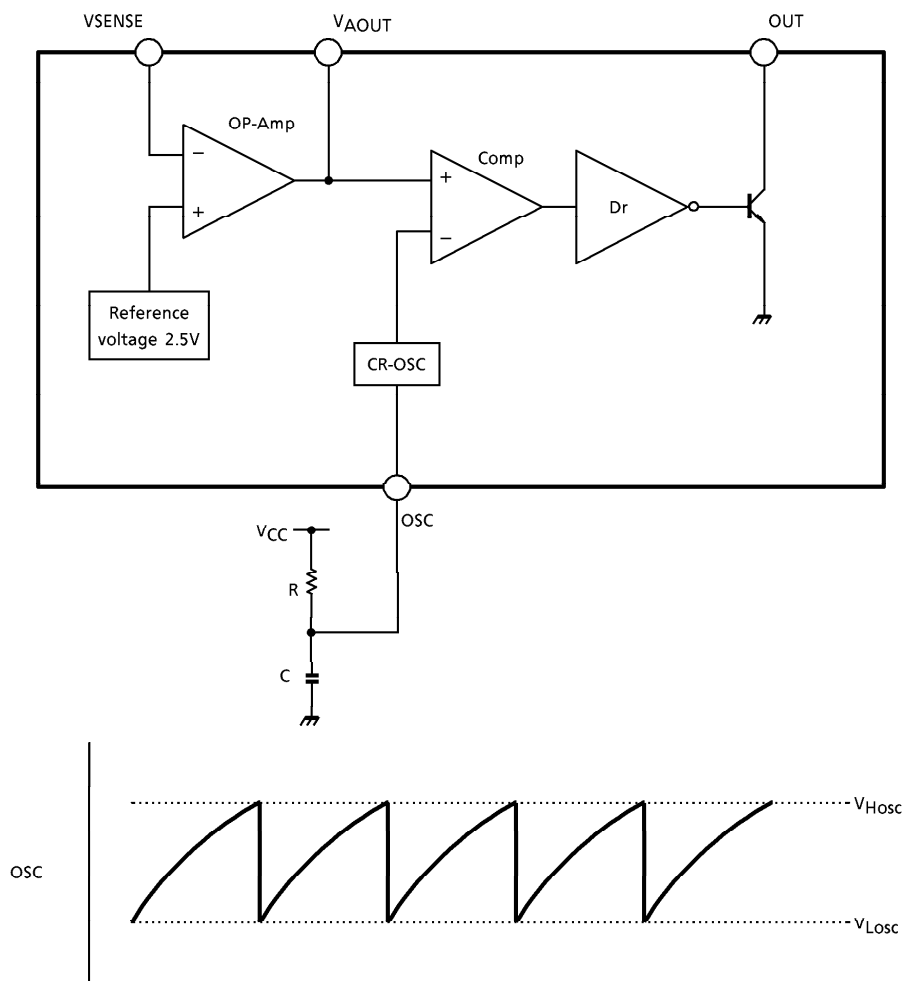
* Output on duty characteristics (reference)



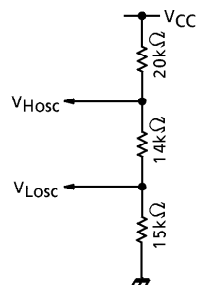
4. CR oscillator circuit

The oscillator for the switching power supply control is connected to an external capacitor and a resistor. This oscillator is used to set the switching frequency.

V_{Hosc} and V_{Losc} are obtained by dividing the V_{CC} using the resistance within the IC.

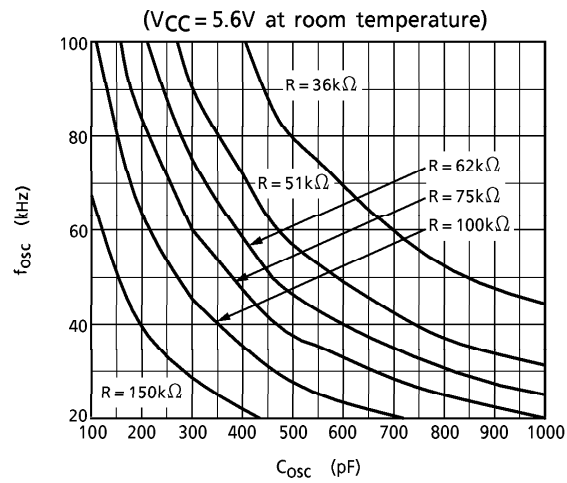


V_{Hosc} and V_{Losc} are set within the IC as shown in the diagram.



For the oscillation frequency setting, see the following graph.

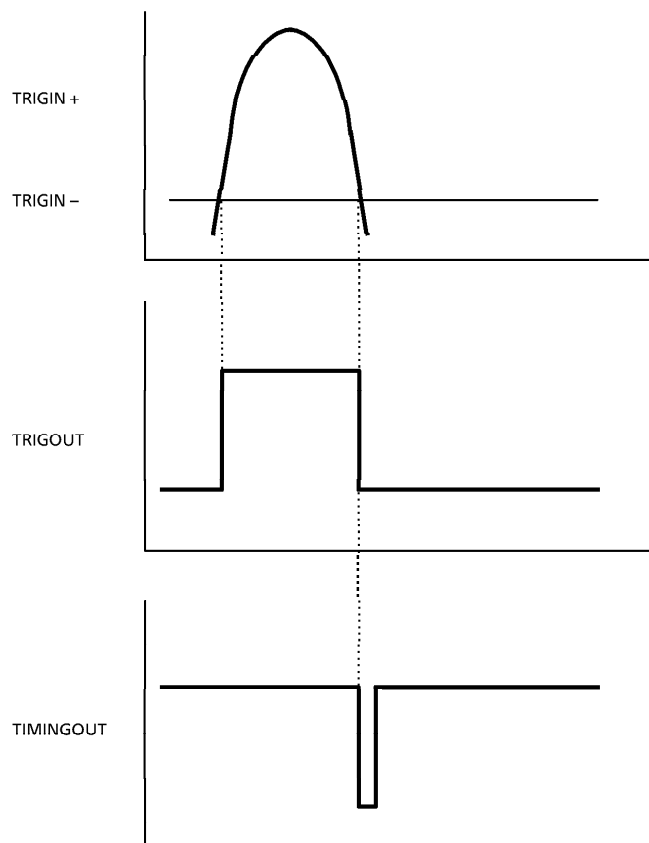
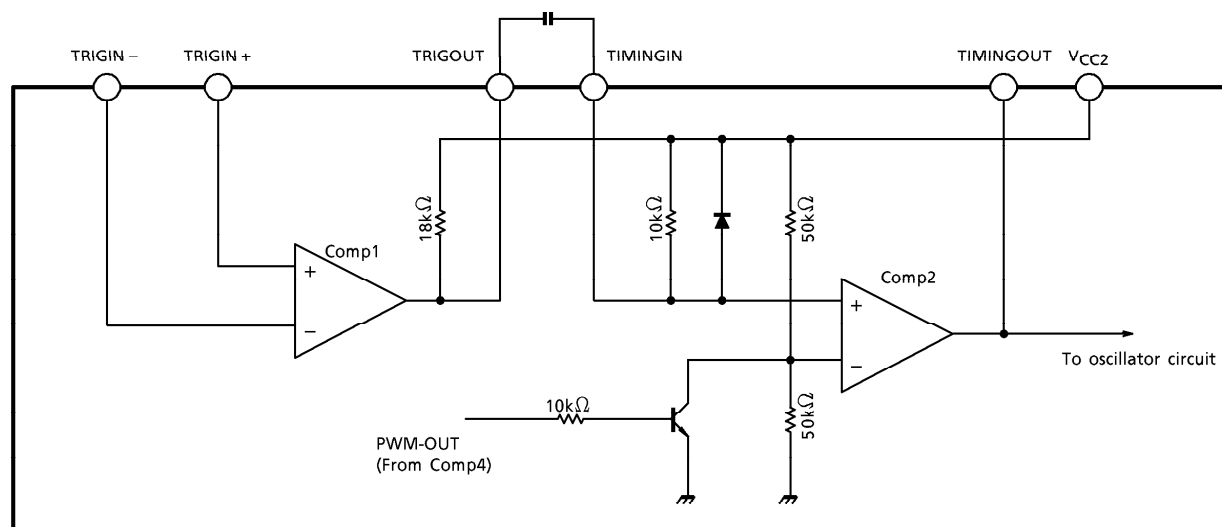
* Oscillation frequency characteristics of switching power supply block (reference)



5. Oscillation timing circuit (trigger comparator/timing comparator)

The trigger comparator detects the oscillation timing ; the timing comparator generates the timing signal.

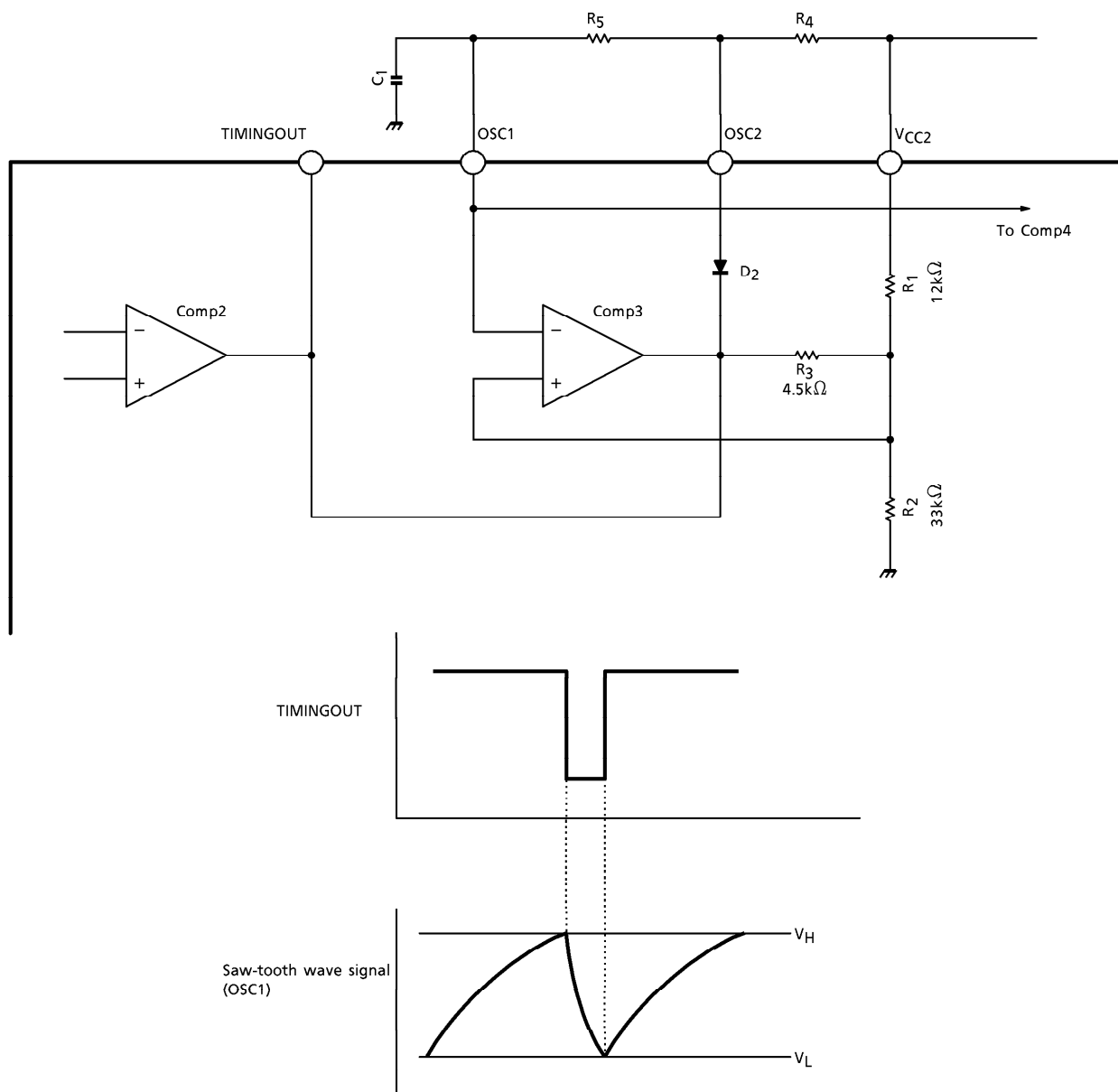
This timing signal is passed on to the next stage oscillator circuit.



6. Oscillator circuit 1 (Oscillation comparator)

This circuit generates a saw-tooth wave signal for the IGBT driver that starts on a timing signal from the oscillation timing circuit.

R_1 , R_2 and R_3 in the IC are used to set the oscillation amplitude of this saw-tooth wave signal.



Here, V_H and V_L are as follows :

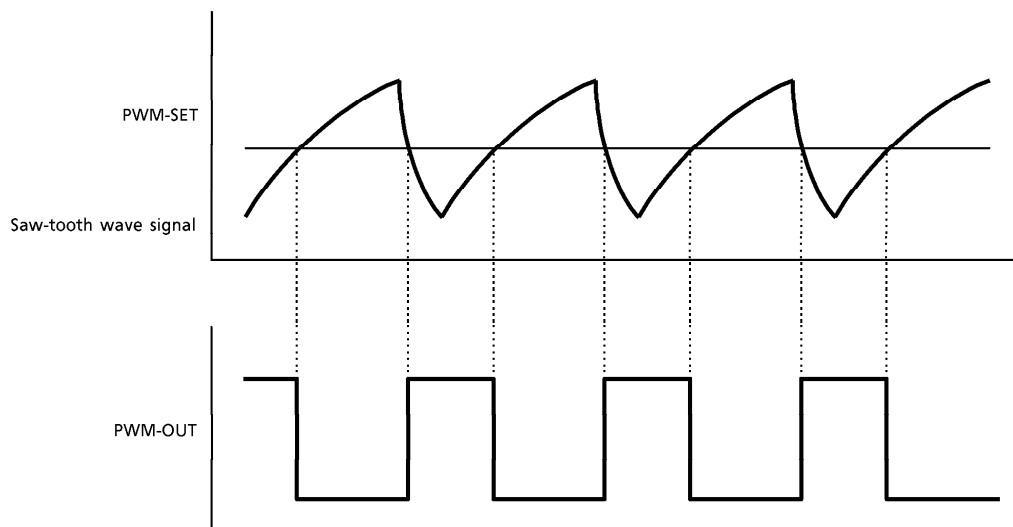
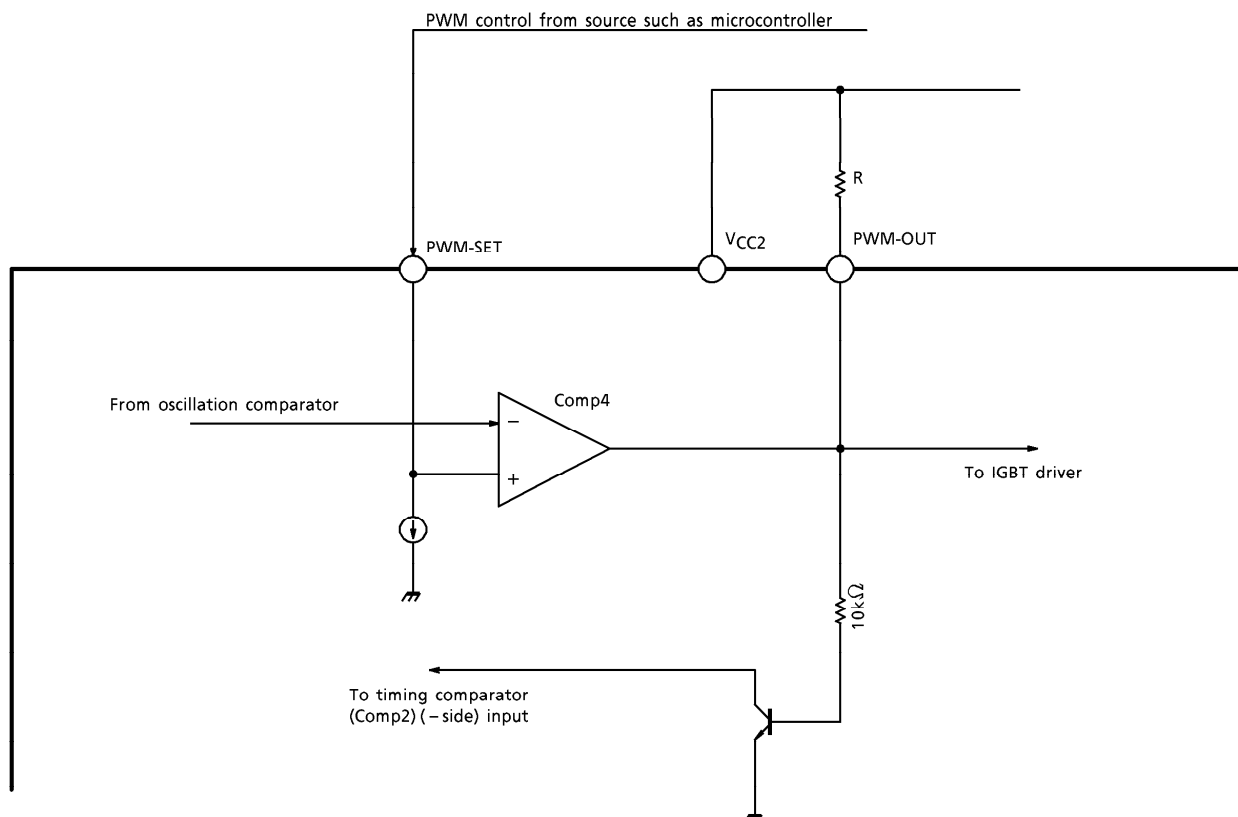
$$V_H = \frac{R_1}{R_1 + R_2} \times V_{CC2} \text{ (V)}$$

$$V_L = \frac{R_3}{R_1 + R_3} \times (V_{CC2} - V_{OL}) + V_{OL} \text{ (V)}$$

7. Oscillator circuit 2 (PWM comparator)

The width of the pulse for the IGBT driver is set by the saw-tooth wave signal from the oscillation comparator and the external output adjustment level.

Input to the PWM-SET pin the PWM output adjustment level from a source such as the microcontroller.



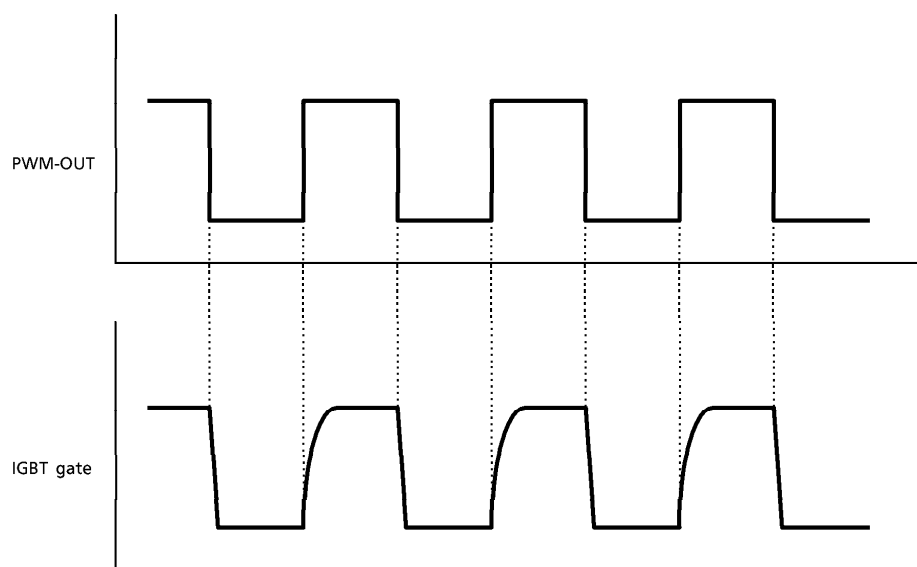
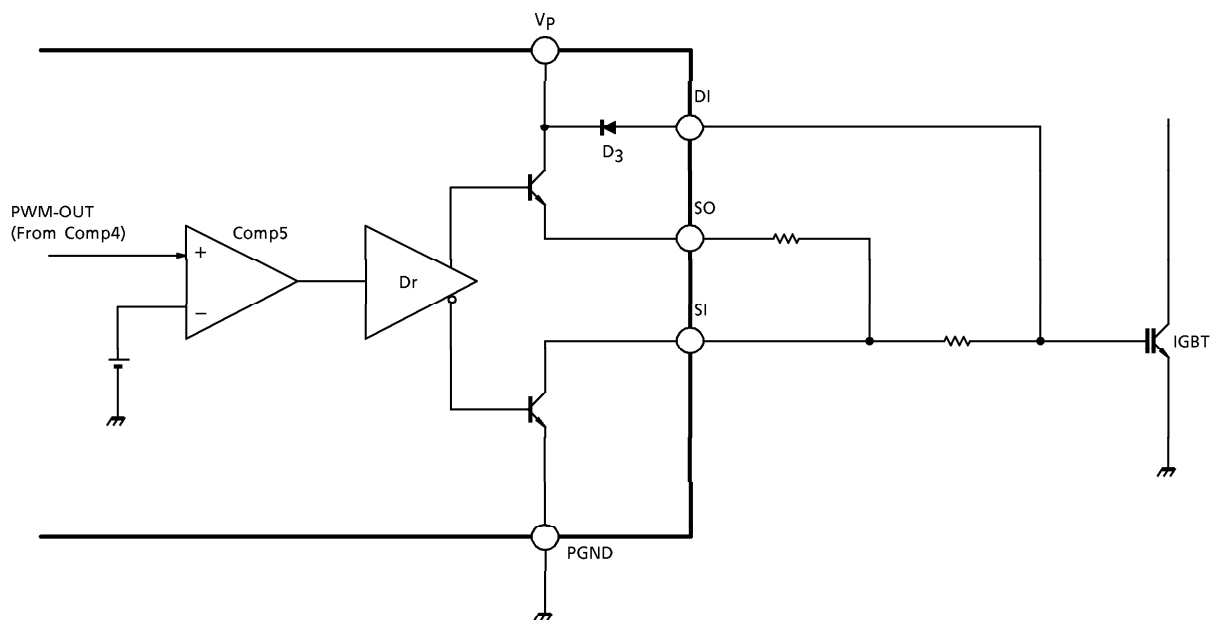
8. IGBT driver

The IGBT driver can be directly driven by a PWM signal from the PWM control block.

Source current : -200mA (max)

Sink current : 1A (max)

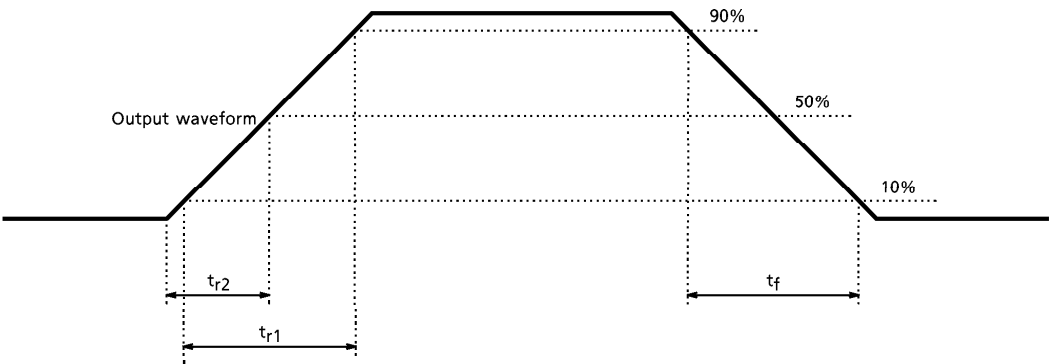
In addition, the device incorporates an IGBT gate protector diode to prevent malfunction at power on.



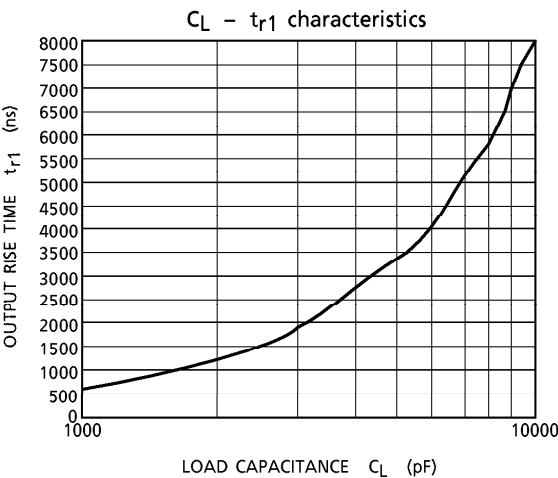
* Reference data

Load capacitance – Output rise / fall time characteristics

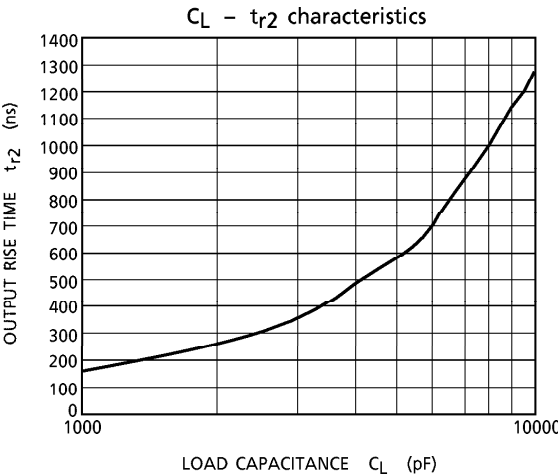
($V_{CC} = 5.6V$, $V_{CC2} = 12V$, $V_P = 20V$, at room temperature)



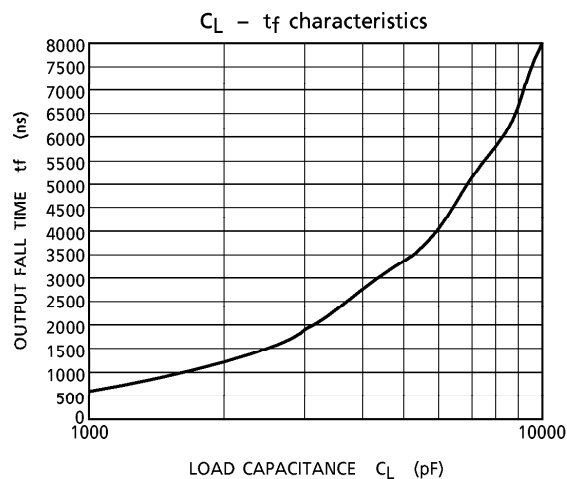
① Load capacitance – Output rise time characteristics 1



② Load capacitance – Output rise time characteristics 2



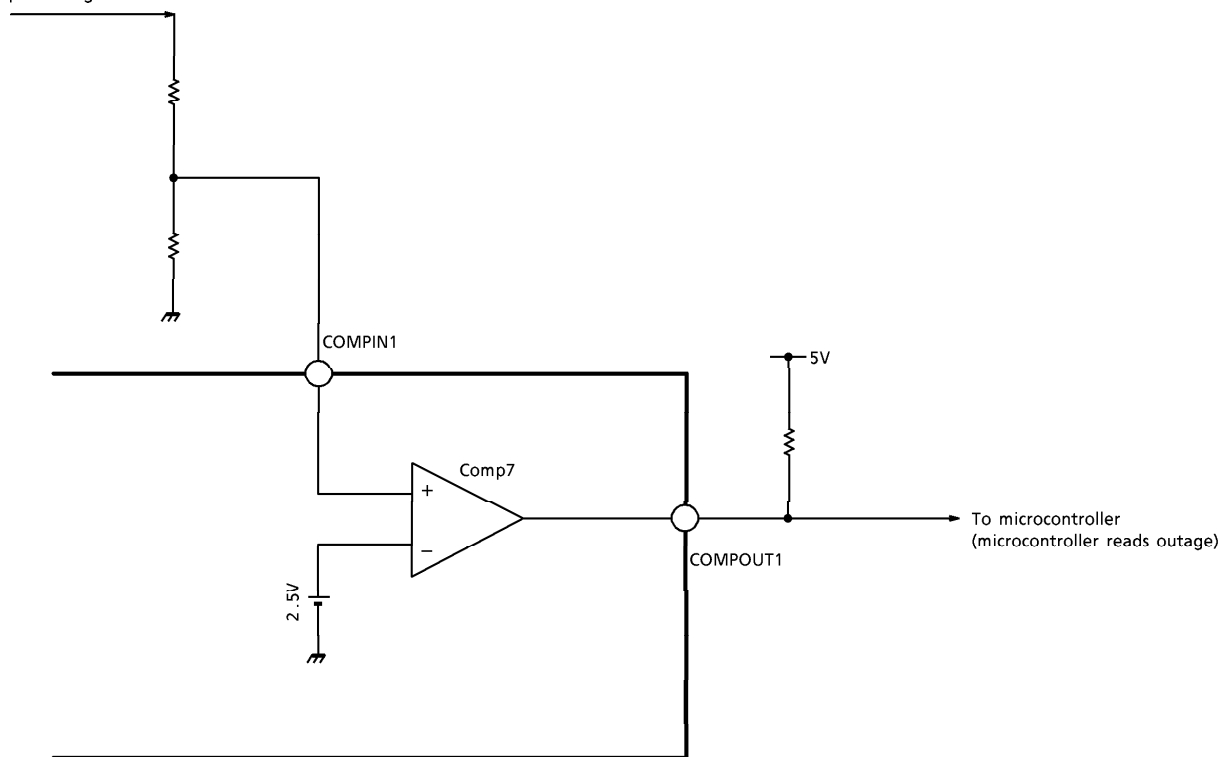
③ Load capacitance – Output fall time characteristics



9. Low voltage detection function

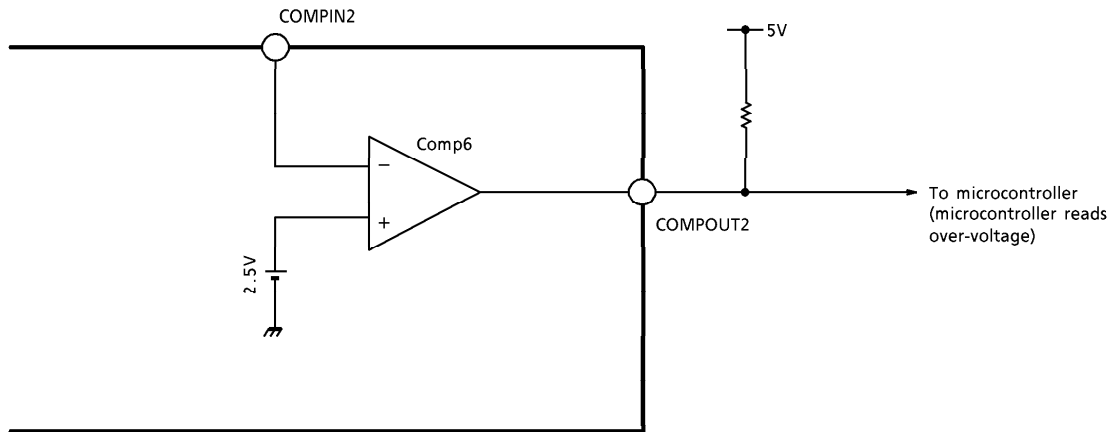
The device incorporates a comparator to detect a fall in voltage, for example at an outage, and output a detection signal to an external device such as a microcontroller.

Monitoring of switching power supply
output voltage



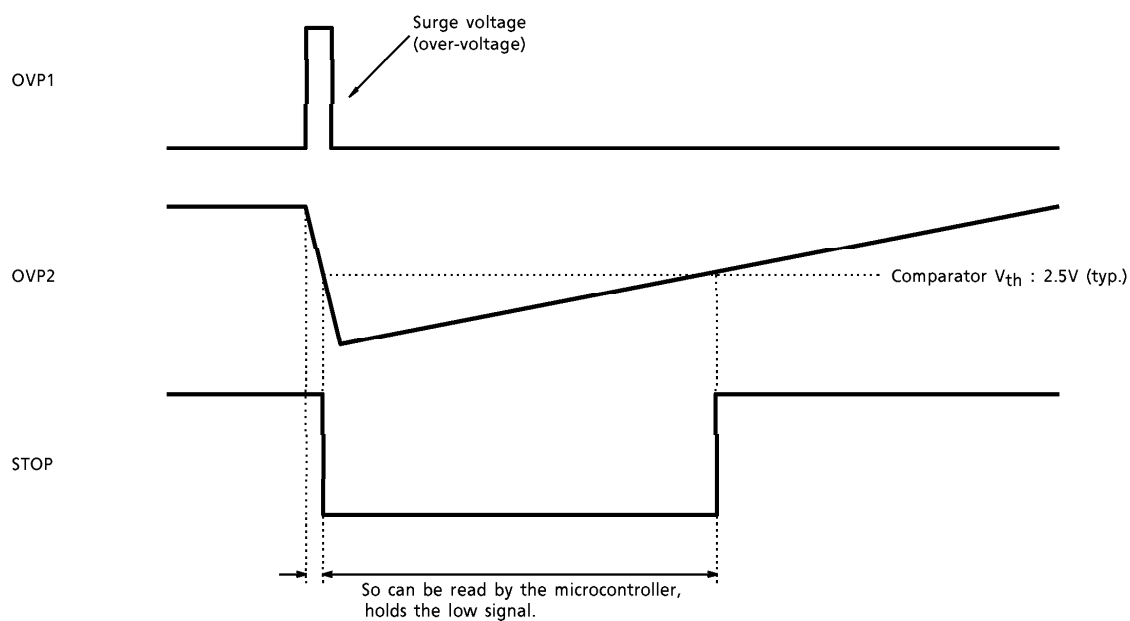
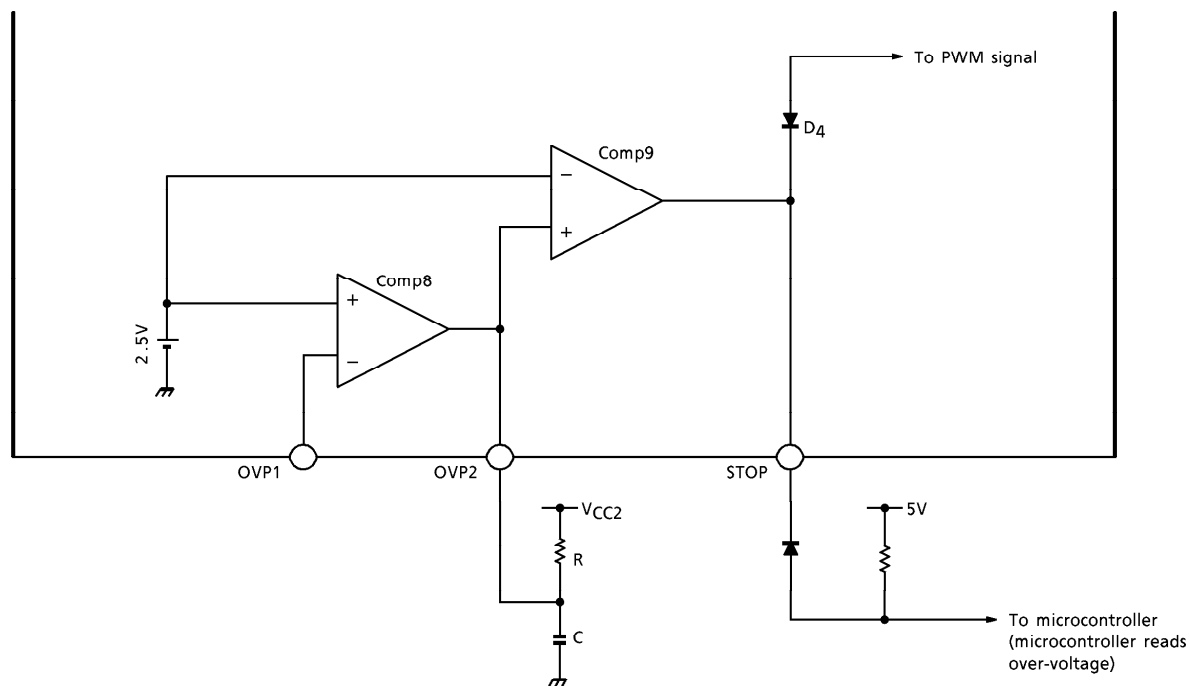
10. Over-voltage detection function 1

The device incorporates a comparator to detect over-voltage and output a detection signal to an external device such as a microcontroller.



11. Over-voltage detection function 2

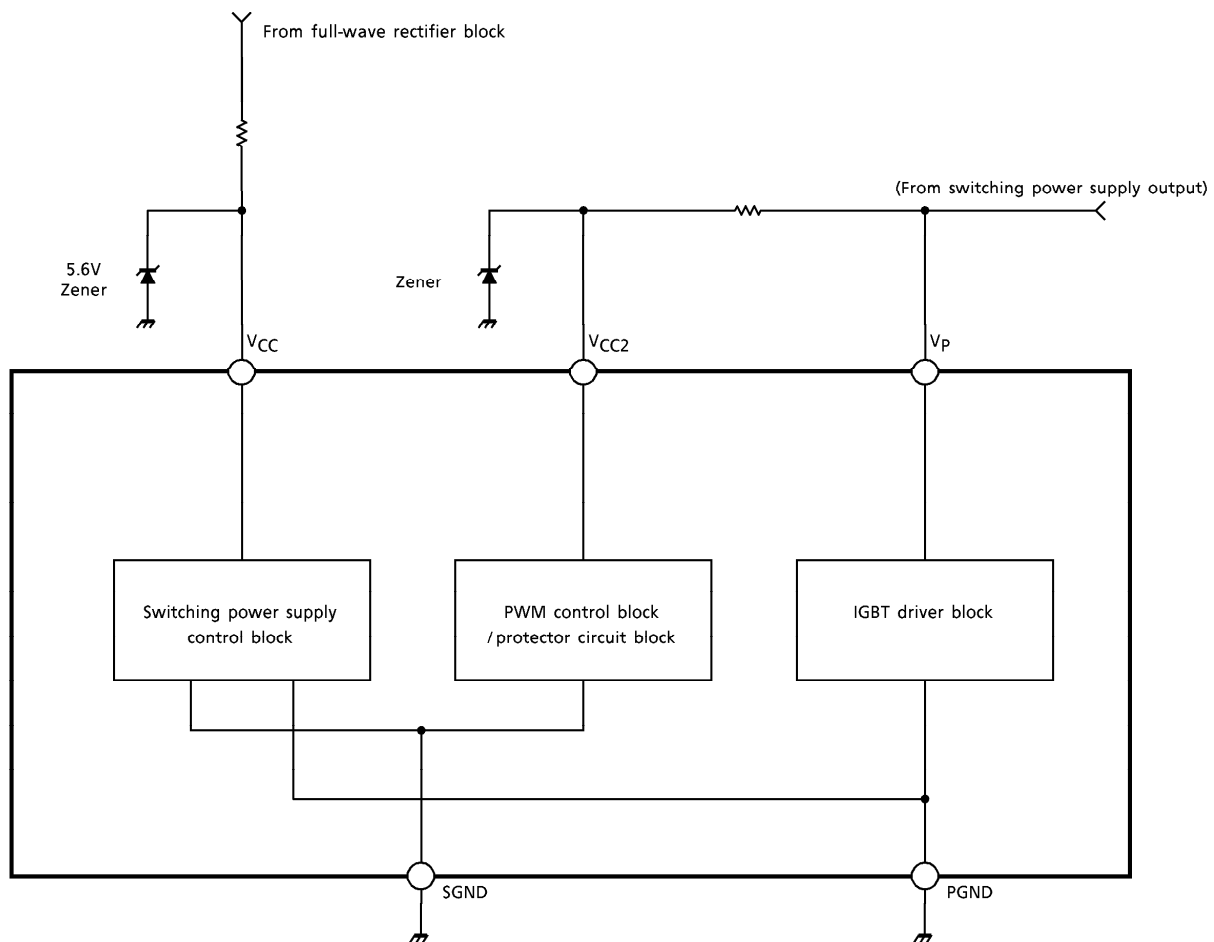
The device incorporates a function to detect over-voltages such as surges and to stop the PWM signal that drives the IGBT.



12. Power supply / GND

The device's power supply and GND are as follows. (See block diagram.)

- (1) Switching power supply control block V_{CC} / SGND / PGND (switching driver only)
- (2) PWM control block / protector circuit block V_{CC2} / SGND
- (3) IGBT driver block V_P / PGND



MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _p	25	V
Input Voltage	V _{in}	GND – 0.3~V _p + 0.3	V
Operating Temperature	T _{opr}	– 20~85	°C
Storage Temperature	T _{stg}	– 55~150	°C
Power Dissipation (*)	P _D	1.56	W

(*) When device is operated at Ta > 25°C, power dissipation decreases 12.5mW per degree.

ELECTRICAL CHARACTERISTICS (Ta = 25°C ; unless otherwise specified : V_{CC} = 5.6V, V_{CC2} = 12V, V_p = 20V)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Power Supply Block							
Switching Power Supply Block Startup Power Supply Voltage	V _{CC} START	—	At circuit startup, switching power supply block only	4.46	4.8	5.15	V
Switching Power Supply Block Shutdown Power Supply Voltage	V _{CC} STOP	—	At circuit shutdown, switching power supply block only	3.8	4.2	4.62	V
Switching Power Supply Block Operating Power Supply Voltage Range	V _{CC}	—	—	5.2	—	6.2	V
Control Block Operating Power Supply Voltage	V _{CC2}	—	Inverter control block, protector circuit block, IGBT driver block (excluding output stage)	4.5	—	15	V
Power Supply Voltage For Power	V _p	—	IGBT driver block (output stage only)	15	—	23	V
Operating Current Consumption 1 (V _{CC})	I _{CC1}	—	V _{CC} = 5.6V, OUT = open (OFF) (switching power supply block)	2	3	5	mA
Operating Current Consumption 2 (V _{CC})	I _{CC2}	—	V _{CC} = 5.6V, OUT = "L" (ON), no load (switching power supply block only)	2	3	5	mA
Operating Current Consumption 3 (V _{CC2})	I _{CC3}	—	V _{CC2} = 12V (PWM control block, protector circuit block)	4	6.5	10	mA
Operating Current consumption 4 (V _p)	I _{CC4}	—	V _p = 20V, PWMOUT = "H", no load	0.7	1.25	1.9	mA
Operating Dissipation Current 5 (V _p)	I _{CC5}	—	V _p = 20V, PWMOUT = "L", no load	4.2	6.25	8.8	mA

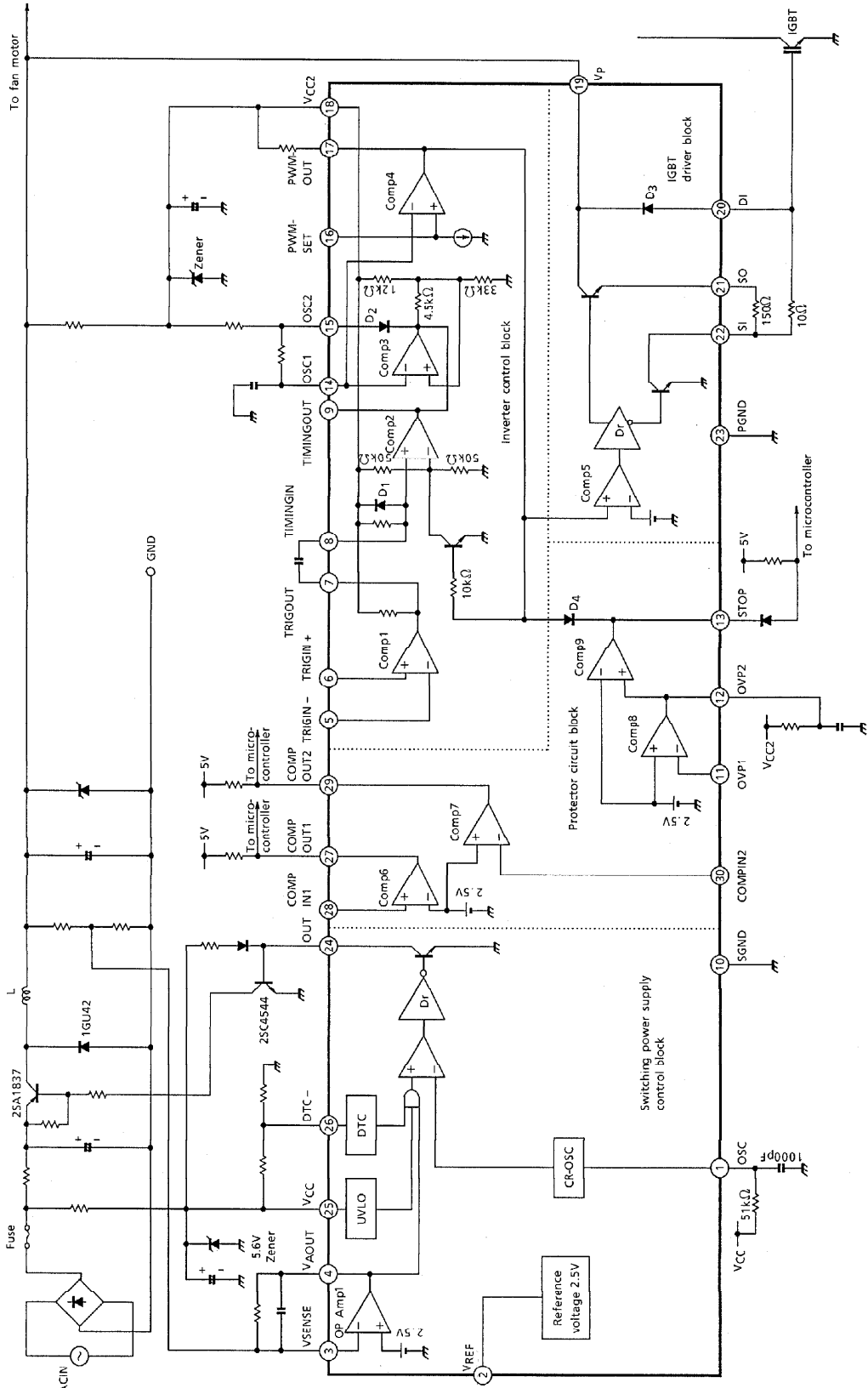
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Switching Power Supply Control Block							
Reference Voltage Block							
(V _{REF} Pin)							
Reference Voltage	V _{ref}	—	No load	2.32	2.5	2.68	V
Power Supply Voltage Fluctuation	ΔV _{ref1}	—	V _{CC} = 4.2 to 6.0V (after circuit startup), no load	− 10	0	10	mV
Temperature Fluctuation	ΔV _{ref2}	—	Ta = − 25 to 85°C, no load	− 20	0	20	mV
Load Fluctuation	ΔV _{ref3}	—	I _{OUT} = − 3mA	− 15	0	5	mV
Oscillator Circuit Block (OSC Pin)							
Oscillation Frequency Range	f _{OSC1}	—	(Design target)	20	—	100	kHz
Voltage Error Amp (OP Amp1)							
(VSENSE Pin)							
Input Dynamic Range	V _{in} EAV	—	—	0	—	V _{CC} − 1.9	V
Input Bias Current	I _B EAV	—	—	− 1	—	1	μA
Input Offset Voltage	V _{off} EAV	—	—	− 10	0	10	mV
Reference Voltage	V _{ref} EAV	—	—	2.32	2.5	2.68	V
(V _{AO} Pin)							
Output Current	I _{out} V _{AO}	—	—	− 1	—	—	mA
Soft-start/Dead Time Control Block							
(SS/DTC Pin)							
Reference Voltage	V _{th} SS/DTC	—	—	2.32	2.5	2.68	V
Output Driver Block							
(OUT Pin)							
Low-level Output Voltage	V _{OL} OUT	—	Output = "L", I _{OL} = 2mA	—	—	0.3	V
Off Leakage Current	I _{off} OUT	—	At V _{in} = V _{CC} = 5.6V, output OFF	− 1	—	1	μA
Inverter Control Block							
Trigger Comparator (Comp1)							
(TRIGIN + Pin)							
Input Dynamic Range	V _{in} TRIG +	—	—	0	—	V _{CC2} − 1.9	V
Input Bias Current	I _B TRIG +	—	—	− 2	—	2	μA
(TRIGIN − Pin)							
Input Dynamic Range	V _{in} TRIG −	—	—	0	—	V _{CC2} − 1.9	V
Input Bias Current	I _B TRIG −	—	—	− 2	—	2	μA
(TRIGOUT Pin)							
High-level Output Current	I _{OH} TRIG	—	V _{CC2} = 12V, output = "H", V _{in} = 0V	512	667	952	μA
Low-level Output Voltage	V _{OL} TRIG	—	Output = "L", I _{OL} = 3mA	—	—	0.3	V

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Timing Comparator (Comp2)							
(TIMING IN Pin)							
Input Dynamic Range	V _{in} TIMING	—	—	0	—	V _{CC2} – 1.9	V
Input Pull-up Resistor	R TIMING	—	—	7	10	13	kΩ
Input Clamp Voltage (D1)	V _F TIMING	—	I _{in} = 3mA	—	—	V _{CC2} + 0.9	V
Threshold Voltage	V _{th} TIMING	—	V _{CC2} = 12V	5.82	6	6.18	V
(TIMING OUT Pin)							
Low-level Output Voltage 1	V _{OL} TIMING1	—	Comp 2 side, output = "L", I _{OL} = 8mA	—	—	0.3	V
Low-level Output Voltage 2	V _{OL} TIMING2	—	Comp 3 side, output = "L", I _{OL} = 8mA	—	—	0.3	V
Oscillation Comparator (Comp3), PWM Comparator (Comp4)							
(OSC1 Pin)							
Input Dynamic Range	V _{in} OSC1-1	—	(Comp3 side)	0	—	V _{CC2} – 1.9	V
Input Bias Current	I _B OSC1-1	—	(Comp3 side)	– 1	—	1	μA
Input Offset Voltage	V _{off} OSC1-1	—	(Comp3 side)	– 10	0	10	mV
Input Dynamic Range	V _{in} OSC1-2	—	(Comp4 side)	0	—	V _{CC2} – 1.9	V
Input Bias Current	I _B OSC1-2	—	(Comp4 side)	– 1	—	1	μA
Input Offset Voltage	V _{off} OSC1-2	—	(Comp4 side)	– 10	0	10	mV
(OSC2 Pin)							
Input Clamp Voltage (D2)	V _F OSC2	—	I _{in} = 7mA	—	—	0.95	V
Oscillator Waveform Upper Limit Voltage	V _H OSC2	—	V _{CC2} = 12V (Design target)	8.53	8.8	9.07	V
Oscillator Waveform Lower Limit Voltage	V _L OSC2	—	V _{CC2} = 12V (Design target)	3.14	3.27	3.49	V
Oscillation Frequency Range	f _{OSC}	—	(Design target)	—	—	50	kHz
(PWM-SET Pin)							
Input Dynamic Range	V _{in} PWM	—	—	0.3	—	V _{CC2} – 1.9	V
Input Offset Range	V _{off} PWM	—	—	– 10	0	10	mV
Sink Current	I _{sink} PWM	—	Internal constant current	– 0.5	– 1	– 2	μA
(PWM-OUT Pin)							
Low-level Output Voltage	V _{OL} PWMOUT	—	Output = "L", I _{OL} = 3mA	—	—	0.3	V

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
IGBT Driver Block							
IGBT Drive Comparator (Comp5)							
Threshold Voltage 1	V_{th} PWM1	—	GATE signal L→H	—	2.63	3.0	V
Threshold Voltage 2	V_{th} PWM2	—	GATE signal H→L	1.5	2.27	—	V
PWM Frequency Range	f PWM	—	(Design target)	—	—	50	kHz
Output Driver Block (Sink Side)							
(SI Pin)							
Low-level Output Voltage 1	V_{OL} SI1	—	PWM = "L", $I_{OL} = 30\text{mA}$	—	—	0.7	V
Low-level Output Voltage 2	V_{OL} SI2	—	PWM = "L", $I_{OL} = 1\text{A}$	—	—	3.5	V
Off Leakage Current	I_{off} SI	—	PWM = "H", $V_{in} = V_p = 20\text{V}$	-1	—	1	μA
Output Driver Block (Source Side)							
(SO Pin)							
High-level Output Voltage 1	V_{OH} SO1	—	PWM = "H", $I_{OH} = -30\text{mA}$	$V_p - 2$	—	—	V
High-level Output Voltage 2	V_{OH} SO2	—	PWM = "H", $I_{OH} = -200\text{mA}$	$V_p - 5$	—	—	V
Off Leakage Current	I_{off} SO	—	PWM = "L", $V_{in} = 0\text{V}$	-1	—	1	μA
IGBT Gate Protection Block							
(DI Pin)							
Input Clamp Voltage (D3)	V_F DI	—	$I_{in} = 500\text{mA}$	—	—	$V_p + 1.5$	V
Protector Circuit Block							
Low-voltage Detection Comparator (Comp6)							
(COMPIN1 Pin)							
Input Dynamic Range	V_{in} COMP1	—	—	0	—	$V_{CC2} - 1.9$	V
Input Bias Current	I_B COMP1	—	—	-2	—	2	μA
Input Offset Voltage	V_{off} COMP1	—	—	-10	0	10	mV
Threshold Voltage	V_{th} COMP1	—	—	2.32	2.5	2.68	V

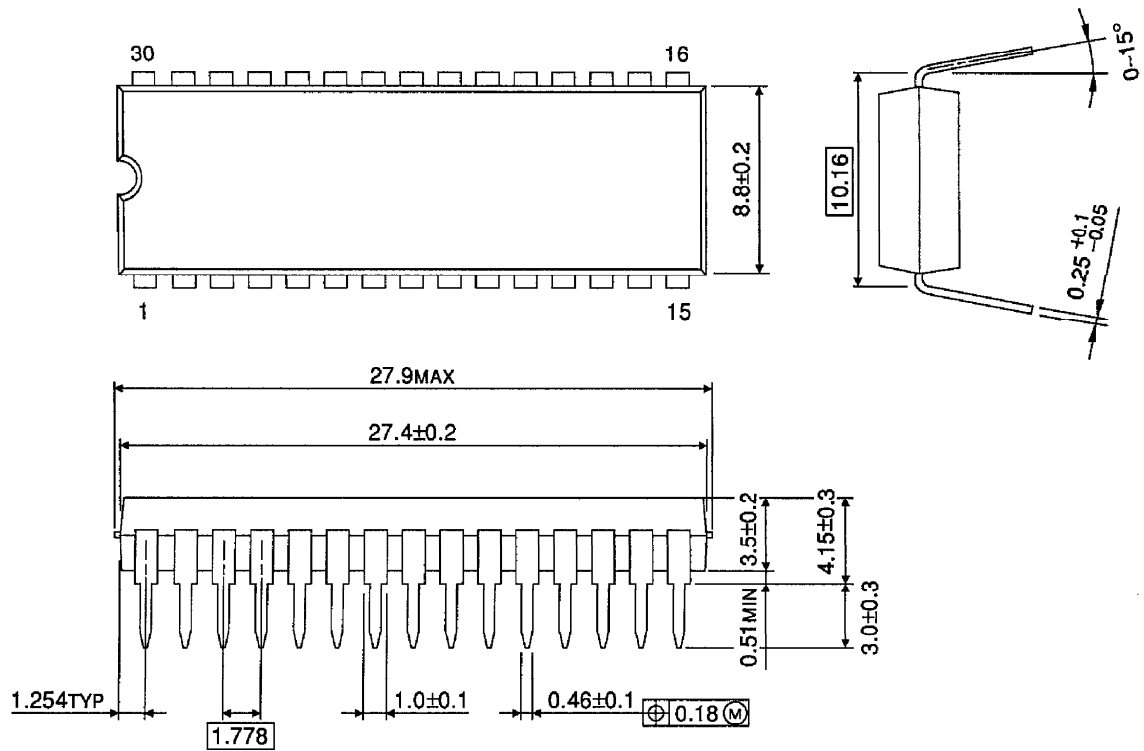
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
(COMPOUT1 Pin)							
Low-level Output Voltage	V _{OL} COMP1	—	Output = "L", I _{OL} = 1mA	—	—	0.5	V
Over-Voltage Detection Comparator 1 (Comp7)							
(COMPIN2 Pin)							
Input Dynamic Range	V _{in} COMP2	—	—	0	—	V _{CC2} - 1.9	V
Input Bias Current	I _B COMP2	—	—	-2	—	2	μA
Input Offset Voltage	V _{off} COMP2	—	—	-10	0	10	mV
Threshold Voltage	V _{th} COMP2	—	—	2.32	2.5	2.68	V
(COMPOUT2 Pin)							
Low-level Output Voltage	V _{OL} COMP2	—	Output = "L", I _{OL} = 1mA	—	—	0.5	V
Over-voltage Detection Comparators 2, 3 (Comp8, Comp9)							
(OVP1 Pin)							
Input Dynamic Range	V _{in} OVP1	—	—	0	—	V _{CC2} - 1.9	V
Input Bias Current	I _B OVP1	—	—	-2	—	2	μA
Input Offset Voltage	V _{off} OVP1	—	—	-10	0	10	mV
Threshold Voltage	V _{th} OVP1	—	—	2.32	2.5	2.68	V
(OVP2 Pin)							
Low-level Output Voltage	V _{OL} OVP2	—	(Comp8 side) Output = "L", I _{OL} = 10mA	—	—	0.3	V
Input Dynamic Range	V _{in} OVP2	—	(Comp9 side)	0	—	V _{CC2} - 1.9	V
Input Bias Current	I _B OVP2	—	(Comp9 side)	-2	—	2	μA
Input Offset Voltage	V _{off} OVP2	—	(Comp9 side)	-10	0	10	mV
Threshold Voltage	V _{th} OVP2	—	(Comp9 side)	2.32	2.5	2.68	V
(STOP Pin)							
Low-level Output voltage	V _{OL} STOP	—	Output = "L", I _{OL} = 1mA	—	—	0.25	V

APPLICATION CIRCUIT



OUTLINE DRAWING
SDIP30-P-400-1.78

Unit : mm



Weight : 1.99g (Typ.)