

## **Recommended Soldering Profiles**

## **Wave Soldering**

- 1. For Through Hole Packages, only the leads of the device are immersed in the wave solder.
- 2. For specified Surface Mount Packages, the entire device is immersed in the wave solder. See Table 1.

|                    |             | Wave Solder       |  |
|--------------------|-------------|-------------------|--|
| Ramp Up °C/sec     | Maximum     | 6° C/sec          |  |
|                    | Recommended | 4° C/sec (Note 1) |  |
|                    | Minimum     | (Note 2)          |  |
| ΔT (Note 3)        | Maximum     | 135° C            |  |
|                    | Recommended | 120° C            |  |
|                    | Minimum     | 110° C            |  |
| Solder Temperature | Maximum     | 260° C            |  |
|                    | Recommended | 240° C            |  |
|                    | Minimum     | (Note 2)          |  |
| Dwell Time Max.    | Maximum     | 4 seconds         |  |
|                    | Recommended | 3 seconds         |  |
|                    | Minimum     | (Note 2)          |  |
| Ramp Down °C/sec   | Maximum     | No Information    |  |
|                    | Recommended | 4° C/sec (Note 1) |  |
|                    | Minimum     | No Information    |  |

Note 1: Will vary depending on board density, geometry, and package type.

Note 2: Will vary depending on package types, and board density.

Note 3:  $\Delta T$  is the temperature differential between the final preheat stage and the soldering stage. Temperature is measured at the component lead area.

TABLE 1. Surface mount packages recommended for Wave Solder Immersion.

| Package Type  | Lead Count |   |   |   |   |    |    |    |
|---------------|------------|---|---|---|---|----|----|----|
|               | 3          | 4 | 5 | 6 | 8 | 14 | 16 | 20 |
| SC-70         |            |   | Х |   |   |    |    |    |
| SOT-23        | Х          |   | Х | Х |   |    |    |    |
| SOT-223       |            | Х | Х |   |   |    |    |    |
| SOIC - NARROW |            |   |   |   | Х | Х  | Х  |    |
| SOIC - WIDE   |            |   |   |   |   | Х  | Х  | Х  |

- · Please verify through device specific application notes.
- All other packages and lead types are not recommended.

## IR / IR Convection / Vapor Phase Reflow - Surface Mount

|  |             | Convection / IR        | Vapor Phase        |
|--|-------------|------------------------|--------------------|
| Ramp Up °C/sec   | Maximum     | 4° C/sec               | 24° C/sec          |
|  | Recommended | 2° C/sec (Note 4)      | 2° C/sec(Note 4)   |
|  | Minimum     | (Note 4)               | (Note 4)           |
| Dwell Time ≥183 °C                                       | Maximum     | 150 seconds            | 85 seconds         |
|  | Recommended | 75 seconds (Note 4)    | 75 seconds(Note 4) |
|  | Minimum     | 60 seconds (Note 4)    | (Note 4)           |
| Peak Temperature<br>(Note 6)                             | Maximum     | 240° C (Note 5)        | 219° C             |
|  | Recommended | <b>220° C</b> (Note 4) | 215° C             |
|  | Minimum     | (Note 4)               | (Note 4)           |
| <b>Dwell Time Max.</b> (within 5° C of peak temperature) | Maximum     | 20 seconds             | 75 seconds         |
|  | Recommended | 15 seconds             | 70 seconds         |
|  | Minimum     | 10 seconds             | (Note 4)           |
| Ramp Down °C/sec   | Maximum     | 4° C/sec               | 4° C/sec           |
|  | Recommended | 2° C/sec               | 2° C/sec           |
|  | Minimum     | (Note 4)               | (Note 4)           |

Note 4: Will vary depending on board density, geometry, and package type.

Note 5: For ceramic packages the maximum is 250° C.

Note 6: All temperatures measured at the component lead area.

## LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT AND GENERAL COUNSEL OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



National Semiconductor Corporation Americas

Tel: 1-800-272-9959 Fax: 1-800-737-7018 Email: support@nsc.com www.national.com National Semiconductor Europe

Fax: +49 (0) 180-530 85 86 Email: europe.support@nsc.com

Deutsch Tel: +49 (0) 69 9508 6208 English Tel: +44 (0) 870 24 0 2171 Français Tel: +33 (0) 1 41 91 8790 Asia Pacific Customer Response Group Tel: 65-2544466 Fax: 65-2504466 Email: ap.support@nsc.com

**National Semiconductor** 

National Semiconductor Japan Ltd. Tel: 81-3-5639-7560 Fax: 81-3-5639-7507